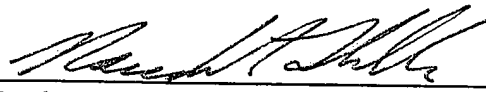


1

No fee is believed to be due under 37 CFR §1.17(p) for this Information Disclosure Statement since it is being filed concurrently with the above-identified application.

Respectfully submitted,

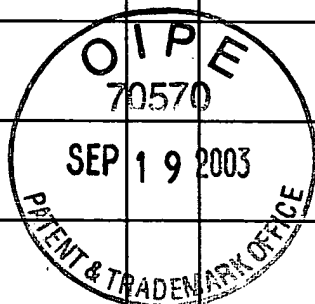
September 19, 2003
Date



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Substitute for form 1449A/PTO				Complete if Known	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(use as many sheets as necessary)</i>				Application Number	Not yet assigned
				Filing Date	Herewith
				First Named Inventor	RATIFICAR, Glenn
				Group Art Unit	Not yet assigned
				Examiner Name	Not yet assigned
Sheet	1	of	1	Attorney Docket Number	P16916

OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
	A	Gilleo, Ken Dr. and Blumel, David., "The Ultimate Flip Chip – Integrated Flux/Underfill", Alpha Metals. 12pgs.	
	B	Babiarz, Alex J. and Huysmans, Frank, "Advances in Dispensing Processes for Flip Chip Underfill", ISHM Nordic 34 th Annual Conference, Asymtek, Carlsbad, CA, 7pgs.	
	C	Hurley, James M. et al., "Kinetic Modeling of No-Flow Underfill Cure and Its Relationship to Solder Wetting and Voiding", Cookson Semiconductor Packaging Materials, Alpharetta, GA, 8pgs.	
	D	Hurley, James M. et al., "Possibilities and Limitations of No-Flow Fluxing Underfill", Cookson Electronics – Semiconductor Products, Alpharetta, GA, ©2002 IEEE, 6pgs.	



Examiner Signature		Date Considered	
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹Unique citation designation number. ²Applicant is to place a check mark here if English language Translation is attached.